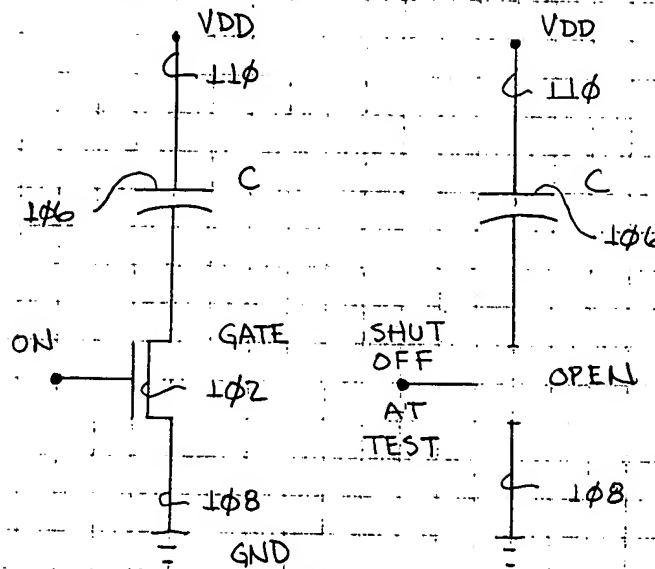


DECOUPLING CAP WITH NFET GATE

PRIOR ART

FIG. 1



PRIOR ART

FIG. 2

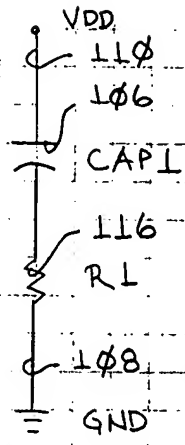


FIG. 3

PRIOR ART

DECOUPLING CAP WITH
INTERATED RESISTOR

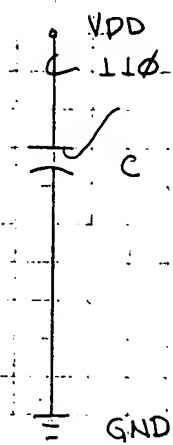
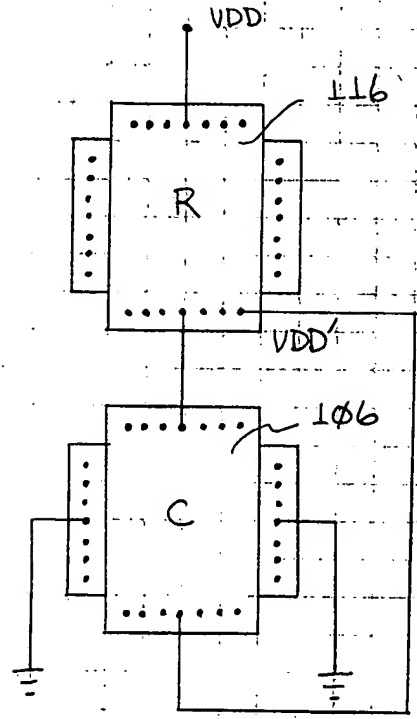


FIG. 5



PRIOR ART

FIG. 4

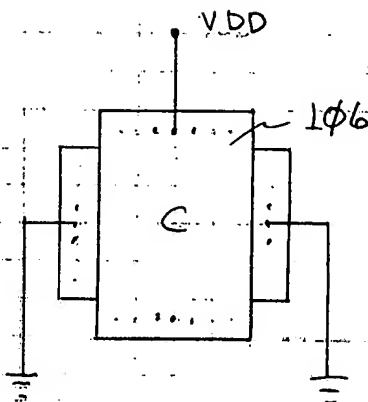


FIG. 6

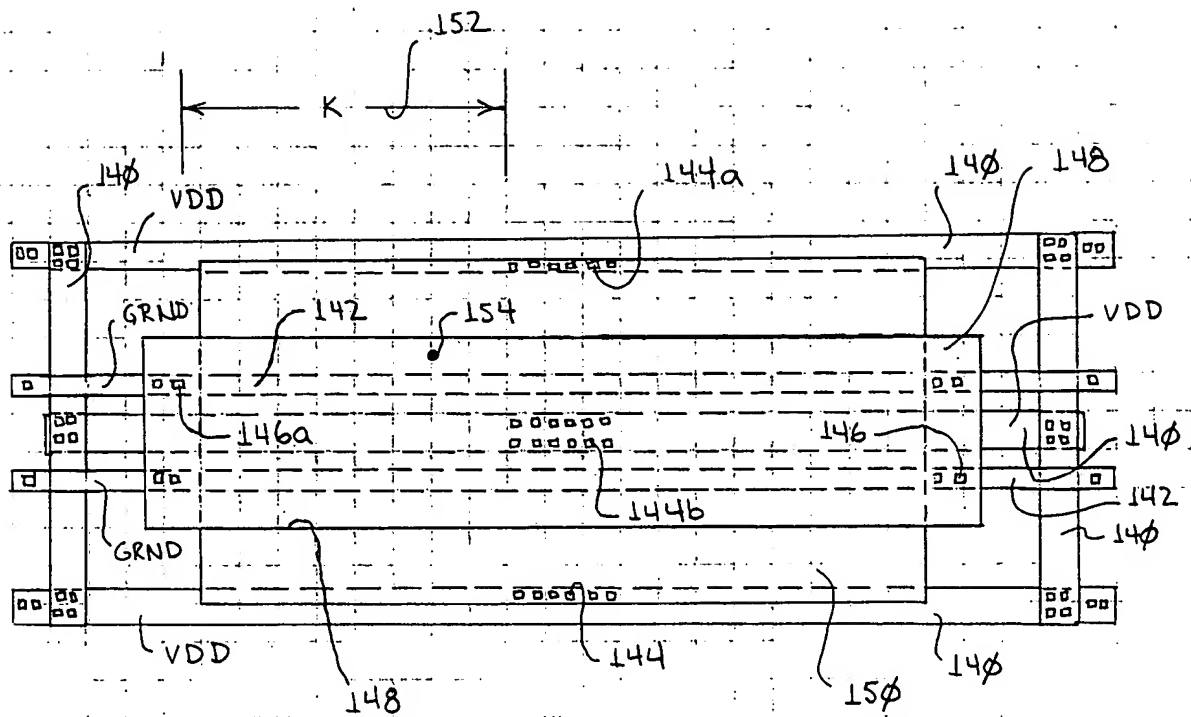


FIG. 7

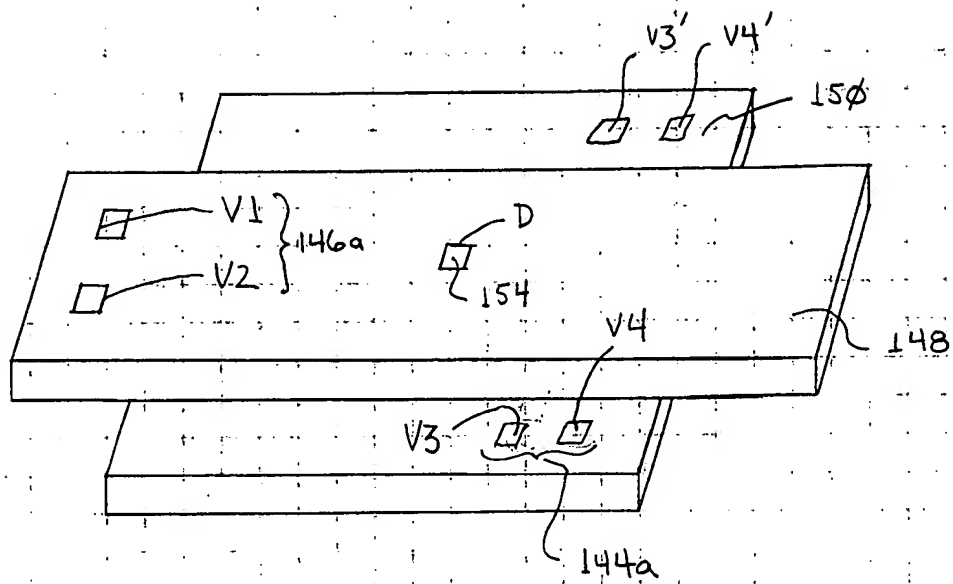


FIG. 8

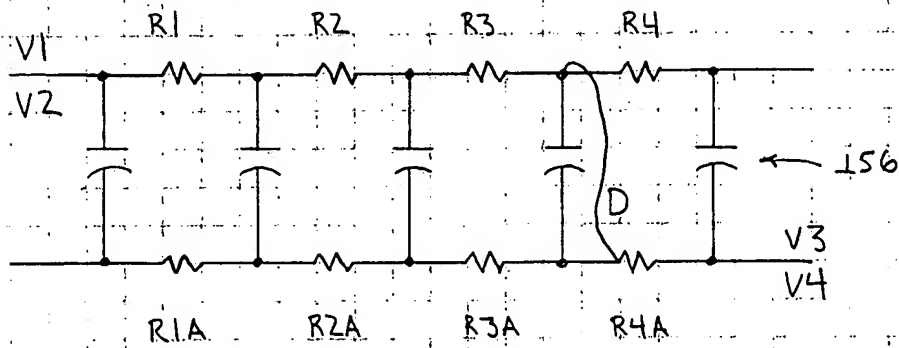
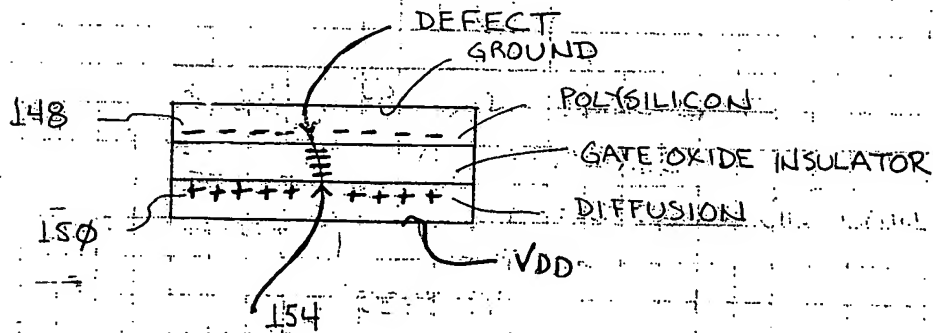
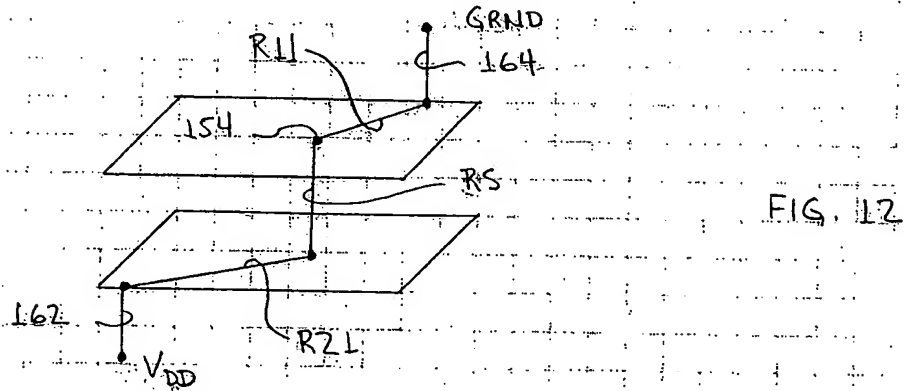
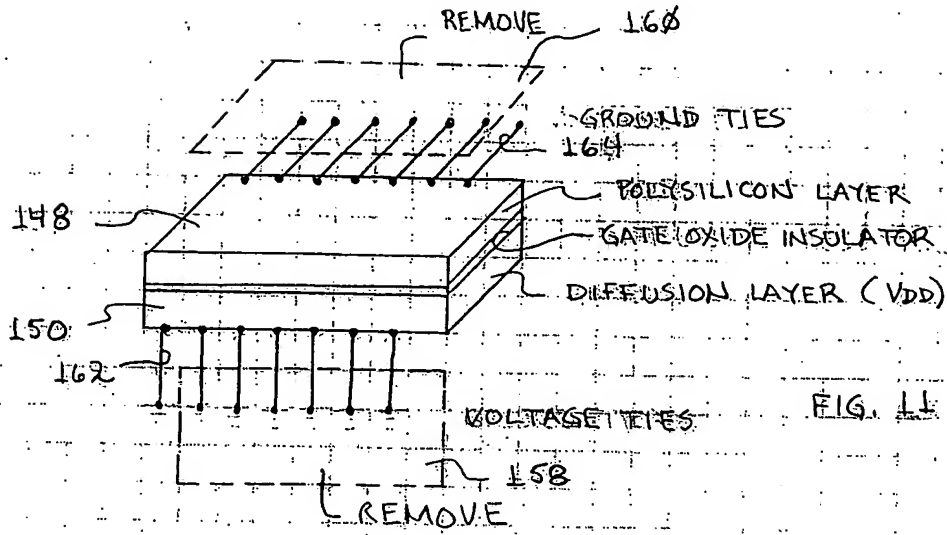


FIG. 9



ROC92003.0250

7/9

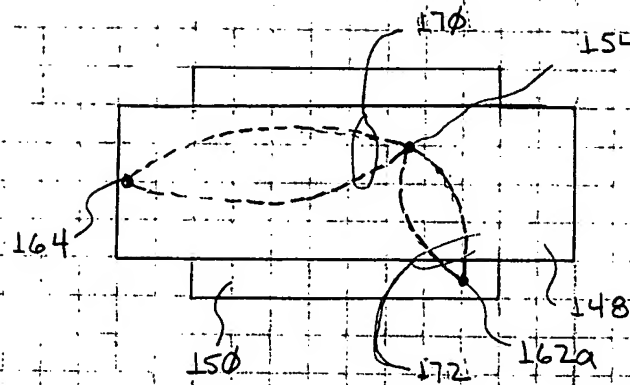


FIG. 13

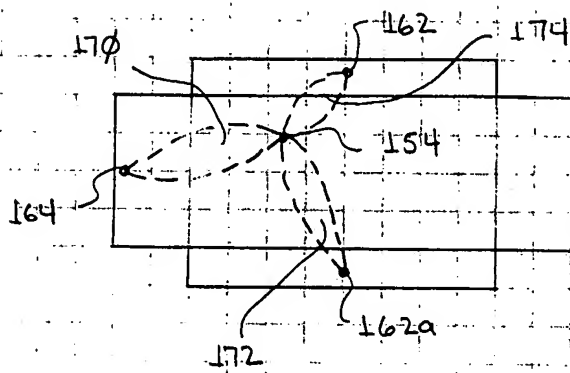


FIG. 14

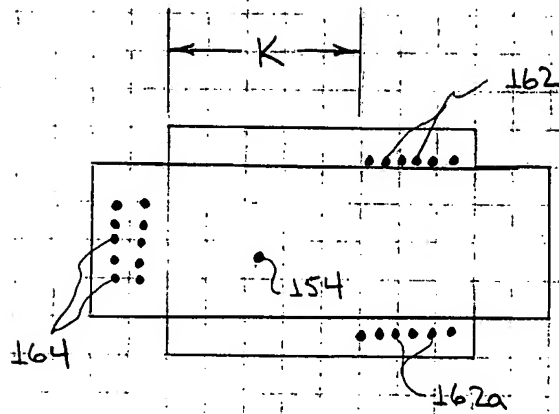


FIG. 15

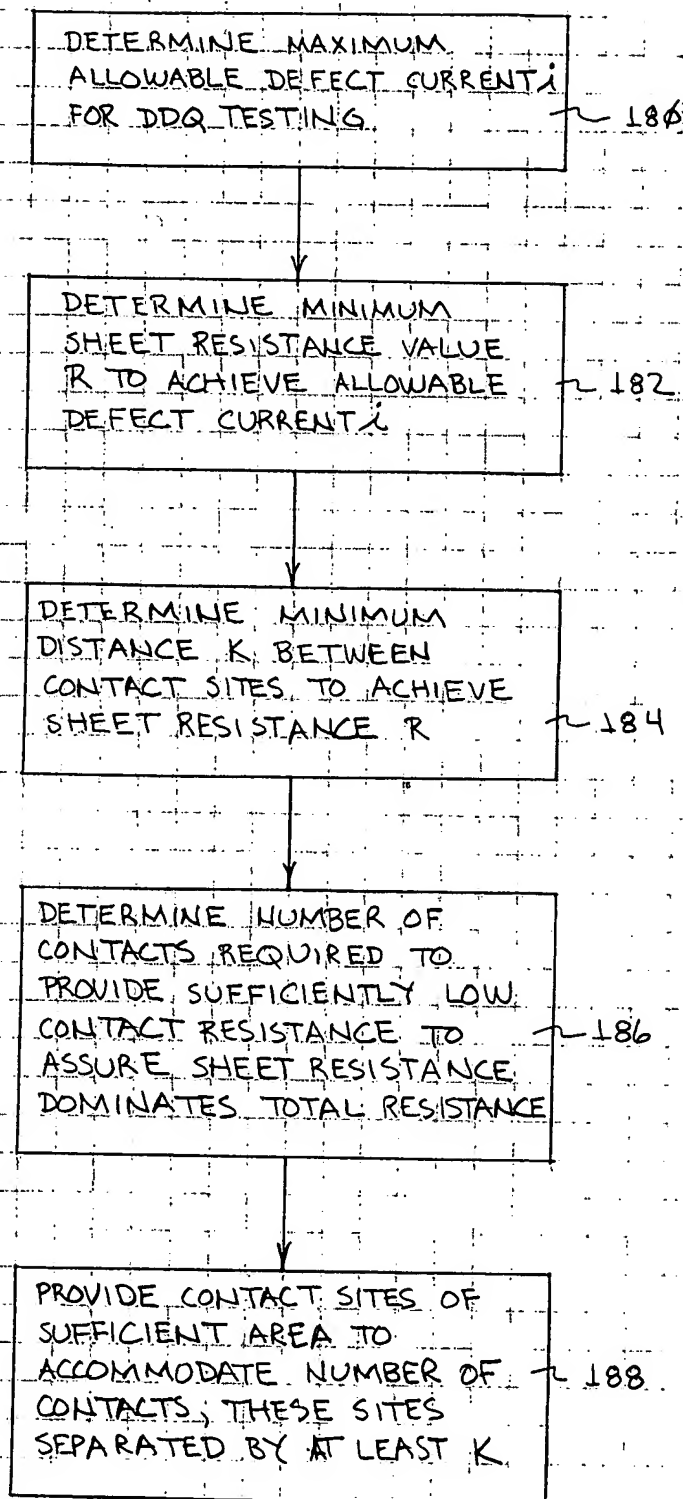


FIG. 16

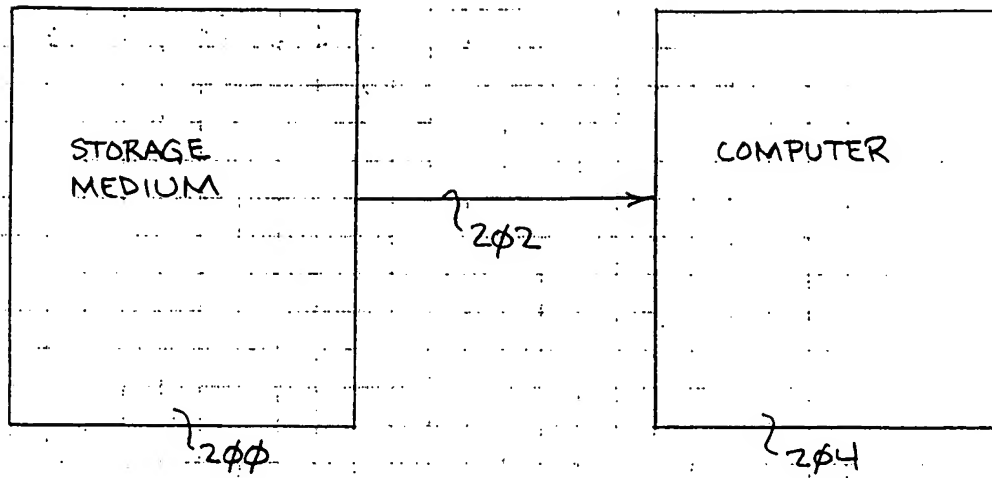


FIG. 17